

## ANSAL UNIVERSITY, SECTOR-55, GURGAON

### Fifth Semester Examination : Regular (December 2014)

Programme : B.Tech. (Electronics & Communication Engineering)

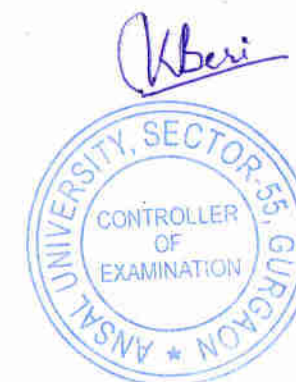
Year : Third

Batch : 2012

Result Tabulation Sheet

Date of Declaration : February 8, 2015

#	Course	Title	Credits	Internal	End Term	Total
1	ECE1015	Microprocessors & Microcontrollers	4	40	60	100
2	ECE1016	Network Analysis & Synthesis	4	40	60	100
3	ECE1017	VLSI Design	4	40	60	100
4	ECE1018	Digital System Design	4	40	60	100
5	ECE1019	Digital Communication Techniques	4	40	60	100
6	ECE1607	Microprocessors & Microcontrollers Lab	1	40	60	100
7	ECE1608	VLSI Design Lab	1	40	60	100
8	ECE1610	Digital System Design Lab	1	40	60	100
9	ECE1611	Digital Communication Techniques Lab	1	40	60	100
10	SS1004	Soft Skills - V	2	60	40	100
11	TDL	Trans Disciplinary Learning	2	60	40	100



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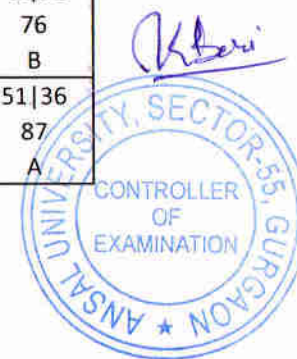
Programme : B.Tech. (Electronics & Communication Engineering)

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LEGEND  
INTERNAL | EXTERNAL  
TOTAL  
GRADE

#	Enrollment No. Student Name	ECE1015	ECE1016	ECE1017	ECE1018	ECE1019	ECE1607	ECE1608	ECE1610	ECE1611	SS1004	TDL
1	AU120030301054P Vidushi Upadhyay	37 58 95 A	38 43 81 B	38.75 55 94 A	37.5 51 89 A	36.5 57 94 A	39 57 96 A	37.67 59 97 A	40 59 99 A	38.83 58 97 A	53 38 91 A	50 37 87 B
2	AU120030303001P Harsh Mamgain	34 57 91 A	35 35 70 B	35 54 89 B	28.25 35 64 C	34.5 55 90 A	36 57 93 A	38.83 59 98 A	29 53 82 B	40 59 99 A	54 38 92 A	55 37 92 A
3	AU120030303002P Lavish Gaba	24.5 RL 25 RL	33.5 38 72 B	25 36 61 C	25.5 11 37 F	28.25 40 69 C	30 50 80 B	34.17 50 85 C	21.5 45 67 C	33.83 50 84 C	54 40 94 A	45.5 30 76 B
4	AU120030303003P Rahul Chaudhary	32.5 40 73 B	33.4 34 68 C	33 45 78 B	29.75 28 58 C	31.25 36 68 C	17 45 62 C	34.17 50 85 C	32 51 83 B	34.5 50 85 C	52 30 82 B	46 31 77 B
5	AU120030303004P Ankush Kaushik	24.5 36 61 C	32.5 25 58 C	30.5 46 77 B	30 36 66 C	28 30 58 C	29 51 80 B	33.5 50 84 C	34 51 85 B	34.5 50 85 C	55 40 95 A	40 20 60 C
6	AU120030303005P Divya Mahalwal	31.5 45 77 B	36.75 38 75 B	30.75 44 75 B	33 43 76 B	28.25 36 65 C	28 46 74 B	33.5 52 86 C	38.5 57 96 A	36.33 52 89 B	53 35 88 B	52 37 89 A
7	AU120030303006P Sheetal Kuhar	34.5 52 87 B	38.5 27 66 C	36 52 88 B	37 48 85 A	36 56 92 A	39 56 95 A	37.67 56 94 B	40 59 99 A	38.83 54 93 B	53 35 88 B	45 37 82 B
8	AU120030303007P Vineet Tokas	30.5 36 67 C	37.5 32 70 B	35 39 74 B	27.75 33 61 C	34.75 43 78 B	32 50 82 B	33.17 52 86 C	28 46 74 B	35.17 50 86 C	50 30 80 B	45 31 76 B
9	AU120030303008P Jafty Elizabeth Varghese	33.5 56 90 A	38.75 48 87 A	37 52 89 B	32.75 53 86 A	35.25 55 91 A	30 52 82 B	37.67 58 96 A	35.25 53 89 B	38.83 59 98 A	51 38 89 B	51 36 87 A



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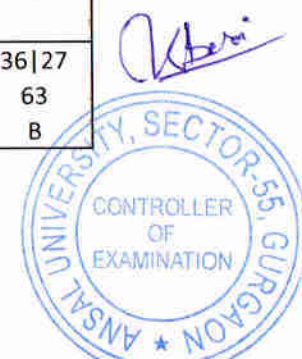
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TOTAL  
GRADE

#	Enrollment No. Student Name	ECE1015	ECE1016	ECE1017	ECE1018	ECE1019	ECE1607	ECE1608	ECE1610	ECE1611	SS1004	TDL
10	AU120030303009P Kunal	30.5 46 77 B	34.5 24 59 C	34.5 52 87 B	30.5 34 65 C	34 45 79 B	36 55 91 A	35.17 58 94 B	24.5 47 72 B	35.17 54 90 B	53 40 93 A	52 36 88 A
11	AU120030303010P Rajat Sharma	20.5 36 57 C	34.5 25 60 C	32.5 42 75 B	27.5 32 60 C	28.5 50 79 B	35 51 86 B	34.17 52 87 C	30.75 51 82 B	37 53 90 B	52 38 90 A	46 32 78 B
12	AU120030303011P Neha Kadian	28.5 50 79 B	38 42 80 B	35 51 86 B	35 45 80 B	30.25 54 85 B	35 55 90 B	34.83 50 85 C	36.5 53 90 B	37 50 87 C	49 35 84 B	60 40 100 A
13	AU120030303017P Apil Sharma	21.5 22 44 D	36.5 29 66 C	33.5 42 76 B	25.25 27 53 C	28.25 37 66 C	33 50 83 B	34.5 54 89 B	19.5 46 66 C	35.17 54 90 B	48 30 78 B	30 25 55 C
14	AU120030303018P Shivaen Katial	25 38 63 C	37.3 34 72 B	31.5 46 78 B	26.75 33 60 C	29.5 40 70 C	32 51 83 B	35.17 54 90 B	30 54 84 B	38.83 54 93 B	51 38 89 B	51 33 84 A
15	AU120030303020P Nupur Chauhan	34.5 52 87 B	36.75 45 82 B	35 51 86 B	37 51 88 A	37.5 58 96 A	30 52 82 B	39.33 59 99 A	38.25 58 97 A	40 56 96 A	53 38 91 A	45 37 82 B
16	AU120030303021P Shubham Gupta	29.5 49 79 B	35.25 40 76 B	33.5 50 84 B	29 39 68 C	34.75 44 79 B	27 49 76 B	33.17 54 88 B	23 45 68 C	37.67 54 92 B	48 38 86 B	33 26 59 C
17	AU120030303022P Risika Nandini	27 52 79 B	34.75 35 70 B	24.25 38 63 C	27.25 24 52 C	24.75 30 55 C	27 49 76 B	32.17 50 83 C	20.5 46 67 C	33.83 52 86 C	53 38 91 A	-
18	AU120030303023P Shubham Kumar	35.5 56 92 A	31.6 44 76 B	32.75 51 84 B	34 42 76 B	32.25 56 89 B	32 49 81 B	39 56 95 A	29 51 80 B	34.5 56 91 B	50 35 85 B	36 27 63 B



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#	Enrollment No. Student Name	ECE1015	ECE1016	ECE1017	ECE1018	ECE1019	ECE1607	ECE1608	ECE1610	ECE1611	SS1004	TDL
19	AU120030303024P Ritika Rana	26 35 61 C	36 41 77 B	26.75 43 70 B	34.75 31 66 C	29 48 77 B	31 47 78 B	34.83 54 89 B	29 50 79 B	34.5 52 87 C	53 38 91 A	45 37 82 B
20	AU120030303027P Nishikant	2.5 AB 3 F	18.5 AB 19 F	9.5 AB 10 F	8.5 AB 9 F	10.5 AB 11 F	1 AB 1 F	13.33 AB 14 F	0 AB 0 F	13.33 AB 14 F	32 AB 32 F	46 AB 46 D
21	AU120030303028P Sakshi Singh	36 58 94 A	39.5 60 100 A	39.5 60 100 A	39.5 56 96 A	39 59 98 A	40 56 96 A	40 59 99 A	39.25 59 99 A	40 59 99 A	53 35 88 B	50 37 87 B
22	AU120030303031P Shagun Dhiman	32 53 85 B	36.3 45 82 B	36 48 84 B	34.75 36 71 B	32.25 41 74 C	31 51 82 B	38.83 54 93 B	29 51 80 B	35.67 54 90 B	49 35 84 B	41 25 66 B
23	AU142030303001P Anirudh Yadav	33.5 57 91 A	39 55 94 A	35 55 90 A	36.25 55 92 A	37.5 53 91 A	34 57 91 A	40 59 99 A	37.75 59 97 A	39.33 59 99 A	53 38 91 A	57 37 94 A
24	AU142030303002P Priyavrat Sagar	28 51 79 B	36.5 34 71 B	35.75 60 96 A	33.75 42 76 B	34.75 59 94 A	32 50 82 B	38.17 59 98 A	34.75 56 91 A	38.83 59 98 A	50 30 80 B	57 37 94 A
25	AU142030303003P Atul	25 40 65 C	32.9 45 78 B	34.75 51 86 B	8 18 26 F	29.5 42 72 C	21 45 66 C	32.17 52 85 C	16 43 59 C	33.83 50 84 C	33 30 63 C	-

CONTROLLER OF EXAMINATIONS



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### Fifth Semester Examination : Regular (December 2014)

Result Tabulation Sheet

Date of Declaration : February 8, 2015

Programme : B.Tech. (Electronics & Communication Engineering)      Year : Third      Batch : 2012

#	Course	Title	No. of Students	A	B	C	D	F	RL
1	ECE1015	Microprocessors & Microcontrollers	25	6	10	6	1	1	1
2	ECE1016	Network Analysis & Synthesis	25	3	15	6	0	1	0
3	ECE1017	VLSI Design	25	4	18	2	0	1	0
4	ECE1018	Digital System Design	25	6	5	11	0	3	0
5	ECE1019	Digital Communication Techniques	25	8	7	9	0	1	0
6	ECE1607	Microprocessors & Microcontrollers Lab	25	6	16	2	0	1	0
7	ECE1608	VLSI Design Lab	25	8	7	9	0	1	0
8	ECE1610	Digital System Design Lab	25	7	12	5	0	1	0
9	ECE1611	Digital Communication Techniques Lab	25	7	9	8	0	1	0
10	SS1004	Soft Skills - V	25	10	13	1	0	1	0
11	TDL	Trans Disciplinary Learning	23	8	11	3	1	0	0
Total				73	123	62	2	12	1

Prepared By: \_\_\_\_\_

Checked By: \_\_\_\_\_

Dean: \_\_\_\_\_

Controller of Examinations: \_\_\_\_\_

