


Date Sheet for B.Tech. EEE (REGULAR) End Term Examination (Practical), December 2017

DATE & DAY	V Semester	VII Semester
30/11/2017 Thursday	15BEC-0MC31L	ECE1608
	Microprocessor and Controller Lab D-221A Ms Arti Deshpandey 10:00 am -12:00 noon	VLSI Design Lab D320 Mr Antim Dev Mishra 10:00 am -12:00 noon
1/12/2017 Friday	15BEE-0PQ31L	ECE1637
	Power Quality Management Lab D-011 Ms Megha 2.00pm-4.00pm	Power System Monitoring & SCADA System Lab D320 Ms Neha Gupta 2:00 pm to 4:00pm
4/12/2017 Monday	15BEE-0PE31L	
	Power Electronics Lab D-321 Ms Harsha Vanjani 10.00am-12.00 Noon	
5/12/2017 Tuesday	15BEE-0TT31C	
	Technology Trends-I D101 Ms Megha 10.00am-12.00PM	
6/12/2017 Wednesday		SS1006-Soft Skills D311 10.00 am-12.00 pm

Note: 1. The student shall maintain proper discipline and orderly conduct during the examinations. They shall not make use of any unfair or dishonest means or indulge in disorderly conduct in the examinations.

2. No student will be allowed to appear in the Examination unless he/she is carrying his/her ID Card and Admit Card during End Term Examination. All the students reappearing in End Term Examination will be allowed with the valid admit card.

3. If a student is found in possession of written/printed matter related to the subject of examination on anything (such as mobile phone, piece of paper or cloth, scribbling pad etc.), other than the answer book, any other response sheet specifically provided by the University to thg.eindidaterit will be treated as act of unfair means and such cases will be forwarded to Unfair Means committee.


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14/11/17
Controller of Examinations